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PAGE 02/03

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1374.43323X00

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Ricardo E. Perez (Depositor's name)
Ricardo Perez (Signature)
10/23/2006 (Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/781,861	02/20/2004	Akio Nishida	501.43323X00	4775

TITLE OF INVENTION: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND A METHOD OF MANUFACTURING THE SAME

APPL. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE (S) DUE	DATE DUE
Nonprovisional	NO	\$1400	\$300	\$1700	12/20/2006

EXAMINER	ART UNIT	CLASS-SUBCLASS
WARREN, MATTHEW E	2815	257-289000

1. Change of correspondence address or indication of "Fee Address: (27 CFR 1.363).

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(A) NAME OF ASSIGNEE

RENESAS TECHNOLOGY CORP.

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

TOKYO, JAPAN

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

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☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27.

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Paul J. Skwierawski

Date: OCTOBER 23, 2006

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FACSIMILE TRANSMITTAL SHEET
OCTOBER 23, 2006

TO: Mail Stop Issue Fee
Commissioner for Patents
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RE: U.S. Patent Application No. **10/781,861**
For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND A METHOD OF
MANUFACTURING THE SAME
Filing Date: 02/20/2006
Atty. Docket No. **1374.43323X00**

From: Leonid D. Thenor

Fax Number: (571) 273-2885

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